

What is claimed is:

1. Computer system including a circuit board comprising:

5 - a bus system for transmitting address, data and control signals with selectable bus configuration;

10 - a plurality of bus sockets on said circuit board for connecting an adapter card with said bus signals, whereby the specification of said adapter card determines the configuration of the associated bus;

20 - an indicator for said bus indicating said configuration, wherein said indicator is integrated in said socket.

15 2. Computer system according to claim 1, wherein said bus system comprises a plurality of busses having independent selectable configurations and each bus comprises at least one socket with an integrated indicator indicating said bus configuration.

20 3. Computer system according to claim 1, wherein said bus system provides a plurality of selectable bus speeds and said indicator further indicates said selected bus speed.

25 4. Computer system according to claim 1, wherein said bus system provides a plurality of selectable bus modes and said indicator further indicates said selected bus mode.

5. Computer system according to claim 1, wherein each socket further comprises an integrated control unit driving said indicator.

5 6. Computer system according to claim 5, wherein said socket comprises a plurality of connectors for connecting said plurality of data, address and control signals and wherein said control unit is coupled with at least one of said connectors.

10

7. Computer system according to claim 1, wherein said indicator comprises a light emitting diode for each of said plurality of clock speeds.

15 8. Computer system according to claim 4, wherein said indicator comprises a light emitting diode for each of said bus modes.

20 9. Computer system according to claim 4, wherein said indicator comprises a liquid crystal display for displaying said clock speed and bus mode.

25 10. Computer system according to claim 4, wherein said indicator comprises an alpha-numeric display for displaying said clock speed and bus mode.

30 11. Computer system according to claim 1, wherein said socket further comprises a switch for enabling a hot-plug mode.

12. Computer system according to claim 11, wherein said socket further comprises a hot-plug indicator for indicating said hot-plug mode.

5 13. Computer system according to claim 11, wherein said socket further comprises hot-plug error indicator means for indicating an error which occurred during a hot-plug mode.

10 14. Computer system according to claim 1, wherein said bus is a PCI-bus.

15 15. Computer system according to claim 1, wherein said bus is a PCI-X-bus.

15 16. Computer system according to claim 1, wherein said socket comprises an enlarged area in which said indicator is integrated.

20 17. Computer system according to claim 16, wherein said socket comprises left and right side walls, whereby an upper portion of on of said side walls is extended outwards to receive said integrated indicator.

25 18. Computer system according to claim 12, wherein said socket comprises an enlarged area in which said hot-plug switch and said hot-plug indicator are integrated.

30 19. Computer system according to claim 18, wherein said socket comprises left and right side walls, whereby an upper portion of on of said side walls is at least

Attorney Document No.
068363.0681
(DC-02940)

PATENT APPLICATION

partially extended outwards to receive said integrated hot-plug switch and indicator.

068363.0681 "DC-02940"

20. Method of indicating a bus speed within a computer system having a system bus with a plurality of sockets for receiving system adapter cards, whereby the specification of said adapter cards determines the bus configuration, and
5 whereby said socket comprises an integrated indicator, comprising the steps of:

- inserting an adapter card in one of said sockets;
- determining and setting the bus speed for all sockets associated to said bus;
- 10 - indicating said bus configuration for said bus by said indicator.

21. Method according to claim 20, further comprising the steps of:

- 15 - providing an indicator for each socket;
- indicating said bus configuration for each socket depending on the determined bus configuration.

22. Method according to claim 20, wherein said system bus comprises a plurality of busses, said method further comprising the steps of:

- 20 - providing indicator means for each socket of each bus;
- indicating said bus configuration for each socket of
25 each bus depending on the determined bus configuration.

23. Method according to claim 20, wherein said bus configuration includes a bus speed.

24. Method according to claim 20, wherein said bus configuration includes a bus mode.

25. Method according to claim 20, wherein said bus configuration determination is performed during initialization of an inserted adapter card.

26. Method according to claim 24, further comprising the step of:

- 10 - determining a bus mode;
- indicating said bus mode by said indicator.

068363.0681-51295860

27. Socket for a computer bus system for connecting a plurality of address, data and control signals to an adapter card which can be plugged into said socket, comprising an indicator for indicating a bus configuration.

5

28. Socket according to claim 27, further comprising a second indicator for indicating a bus speed.

29. Socket according to claim 27, further comprising a
10 third indicator for indicating a bus mode.

30. Socket according to claim 27, further comprising a switch for enabling a hot-plug mode.

15 31. Socket according to claim 27, further comprising a hot-plug indicator indicating whether a hot-plug mode is enabled.

20 32. Socket according to claim 27, further comprising a hot-plug error indicator indicating whether an error during a hot-plug mode occurred.

25 33. Socket according to claim 27, wherein said socket comprises an enlarged area in which said indicator is integrated.

30 34. Socket according to claim 33, wherein said socket comprises left and right side walls, whereby an upper portion of on of said side walls is extended outwards to receive said integrated indicator.

5

10

15

20

25